

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): **Dickinson, et al.**

Docket No.: **LUX-P017**

Serial No.: **(Not yet assigned)**

Group Art Unit: **(Not yet assigned)**

Filed: 1/14/04

Examiner: **(Not yet assigned)**

Title: **Integrated Photonic-Electronic Circuits and Systems**

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring the enclosed printed publications and U.S. patent references to the attention of the Examiner.

Applicant respectfully submits that Applicants' claimed invention is patentable over such references. Forms PTO/SB/08A and PTO/SB/08B listing these references are attached. It is understood the Office may find additionally relevant material in these or other references

Respectfully submitted,



Mircea Achiriloaie
Reg. No. 48,880

1/13/04
Date

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Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet 1 of 5

Application Number	NOT YET ASSIGNED
Filing Date	5/14/04
First Named Inventor	DICKINSON, et al
Group Art Unit	NOT YET ASSIGNED
Examiner Name	NOT YET ASSIGNED
Attorney Docket Number	LUX - PO17

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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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		Filing Date	4/19/04
		First Named Inventor	DICKINSON, et al
		Art Unit	NOT YET ASSIGNED
		Examiner Name	NOT YET ASSIGNED
		Attorney Docket Number	LUX-PO17.
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1.	"SCALING OPTOELECTRONIC - VLSI CIRCUITS INTO THE 21ST CENTURY: A TECHNOLOGY ROADMAP". ASHOK V. KRISHNAMOORTHY, MEMBER, IEEE, AND DAVID A.B. MILLER, FELLOW IEEE; IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS, VOL. 2, NO. 1, APRIL 1996	
	2.	"OPTICAL INTERCONNECTS TO SILICON". DAVID A.B. MILLER IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS VOL. 6, NO. 6, NOVEMBER/DECEMBER 2000	
	3.	"CHALLENGES IN OPTICALLY INTERCONNECTING ELECTRONICS" FRANK A.P. TOOLEY, MEMBER, IEEE. IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS VOL 2, NO. 1, APRIL 1996.	
	4.	"OPTICAL INTERCONNECTS: OUT OF THE BOX FOREVER?" DAWEI HUANG, MEMBER, IEEE, THERESA SZE, MEMBER, IEEE, ANDERS LANDIN, RICK LYTEL, MEMBER IEEE, AND HOWARD L. DAVIDSON, SENIOR MEMBER, IEEE	

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	5.	<p>"VERY SHORT REACH OPTICAL INTERCONNECTS IN SYSTEMS: THE CASE FOR FIBER-TO-THE-PROCESSOR".</p> <p>The University of Southern California; http://www.usc.edu/~alevi; A.F.J. Levi; OPTICAL DIGITAL COMMUNICATION SEMINAR ON MAY 21, 2001.</p>	
	6.	<p>"HIGH-SPEED OPTOELECTRONIC VLSI SWITCHING CHIP WITH >4000 OPTICAL I/O BASED ON FLIP-CHIP BONDING OF MQW MODULATORS AND DETECTORS TO SILICON CMOS."</p> <p>Anthony L. Lentine, Keith W. Goossen, J. A. Walker Leo, Arthur D'Asaro, P. Hui, B. J. Tseng, R. E. Leibenguth, J. E. Cunningham, W. Y. Jan, Jen-Ming Kuo, D. W. Dahringer, D. P. Kossives</p>	
	7.	<p>HETEROGENEOUS INTEGRATION OF OE ARRAYS WITH Si ELECTRONICS AND MICROOPTICS; YUE LIU; IEEE TRANSACTIONS ON ADVANCED PACKAGING, VOL. 25, NO. 1, FEBRUARY 2002.</p>	

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Application Number	NOT YET ASSIGNED
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First Named Inventor	Dickinson, et al
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	LUX-7017.

NON PATENT LITERATURE DOCUMENTS

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	8.	"FIBER WEIGHED FOR CHIP INTERCONNECT", WWW.CMPNET.COM. By ANTHONY CATALDO, OCT 12, 2001, URL: http://www.eetimes.com/story/OEG_20011012S0061	
	9.	"EVALUATION OF ADVANCED OPTOELECTRONIC INTERCONNECT TECHNOLOGY" By JANICE ONANIAN McMATHON, TOM EMBERLEY, MIT LINCOLN LABORATORY; 30 JULY 2001.	
	10.	"PROSPECTS OF CMOS TECHNOLOGY FOR HIGH SPEED OPTICAL COMMUNICATION CIRCUIT". BEHZAD RAZAVI, SENIOR MEMBER, IEEE; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 37, NO. 9. SEPTEMBER 2002.	
	11.	"ADVANCE IN SILICON-ON-INSULATOR OPTOELECTRONICS." B. JALALI, S. YEGHANARAYANAN, T. YOON, T. YOSHIMOTO, I. RENDINA, AND F. COPPINGER. IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS VOL. 4. NO. 6. NOVEMBER/DECEMBER 1998.	

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